IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: TOD PAULUS ET AL.

Filed:

FEBRUARY 12, 2002

For:

DC OFFSET REDUCTION IN RADIO-FREQUENCY APPARATUS

AND ASSOCIATED METHODS

Serial No.:

10/074,676

Group Art Unit:

2684

Examiner:

LEE, J.

Atty Dkt:

SILA:098

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

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SUBMISSION OF FORMAL DRAWINGS

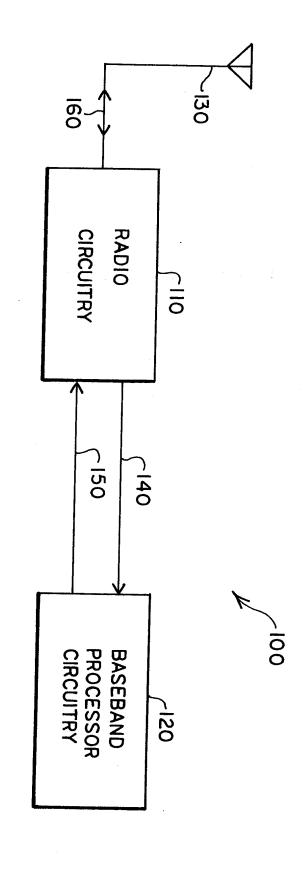
Applicants hereby submit the formal drawings for the above-referenced application and request that these drawings be accepted for filing.

Respectfully submitted,

Maximilian R. Peterson

Reg. No.: 46,469

O'KEEFE, EGAN & PETERMAN 1101 Capital of Texas Highway South Building C, Suite 200 Austin, Texas 78746 (512) 347-1611 (512) 347-1615 (Fax)



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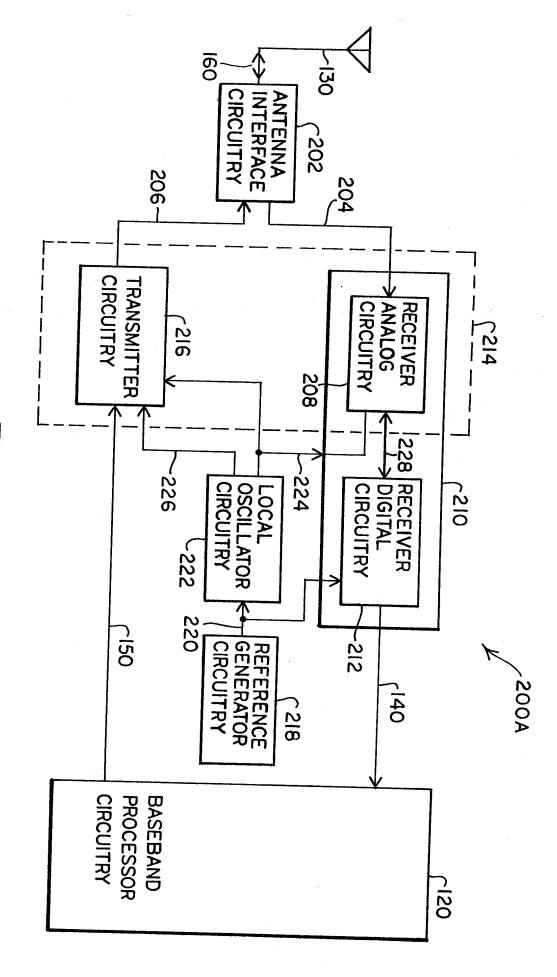
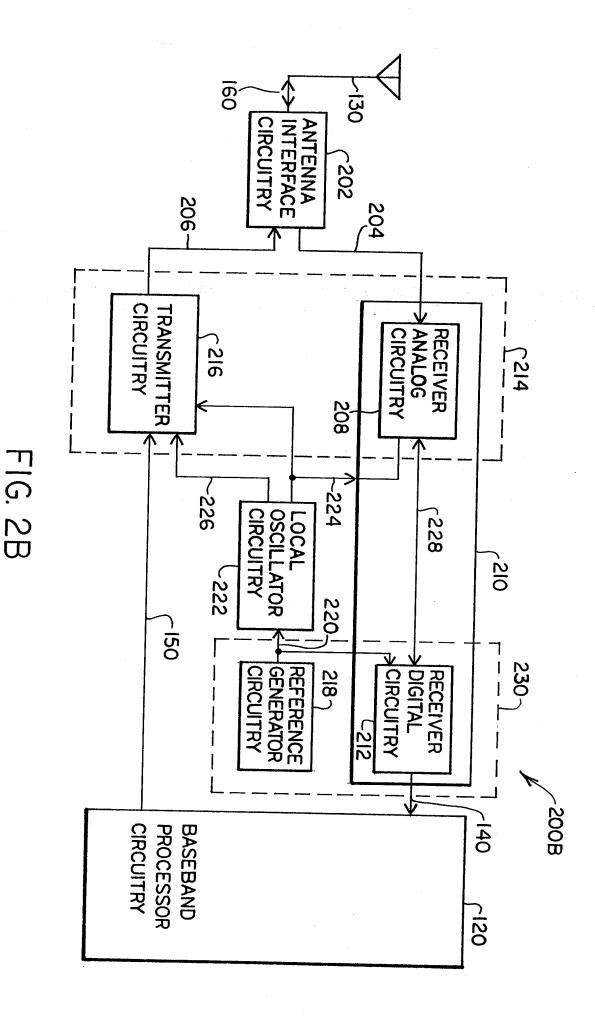
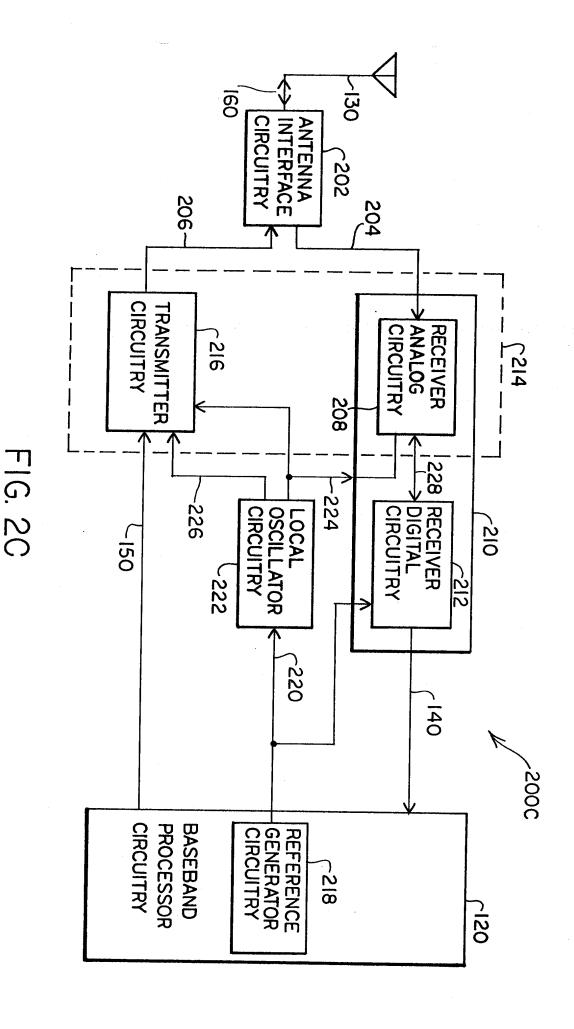


FIG. 2A





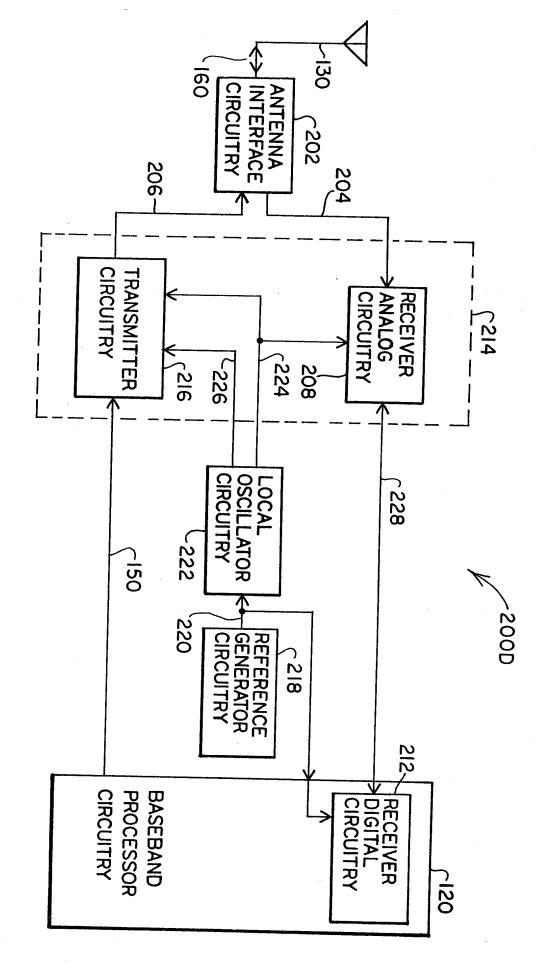
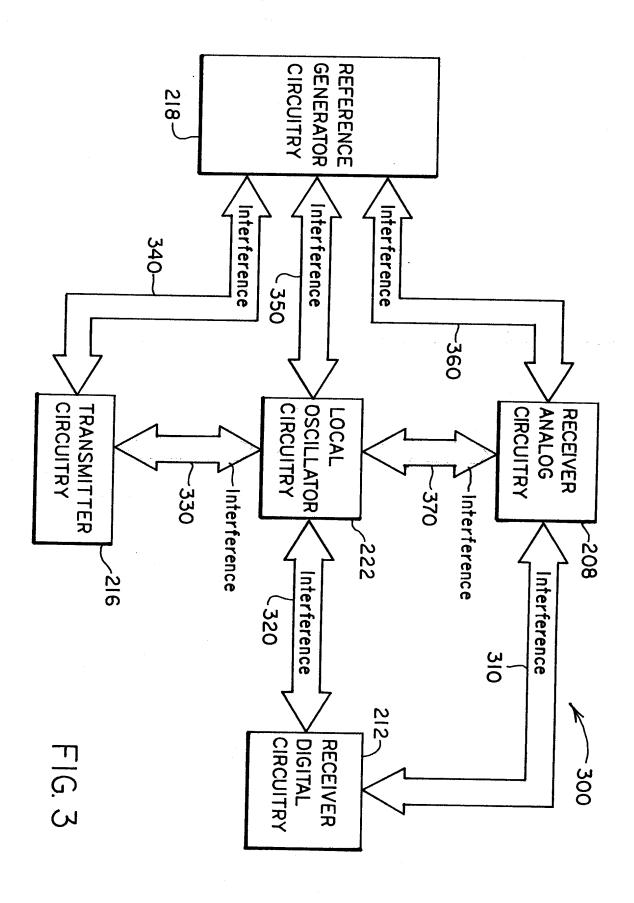


FIG. 2D



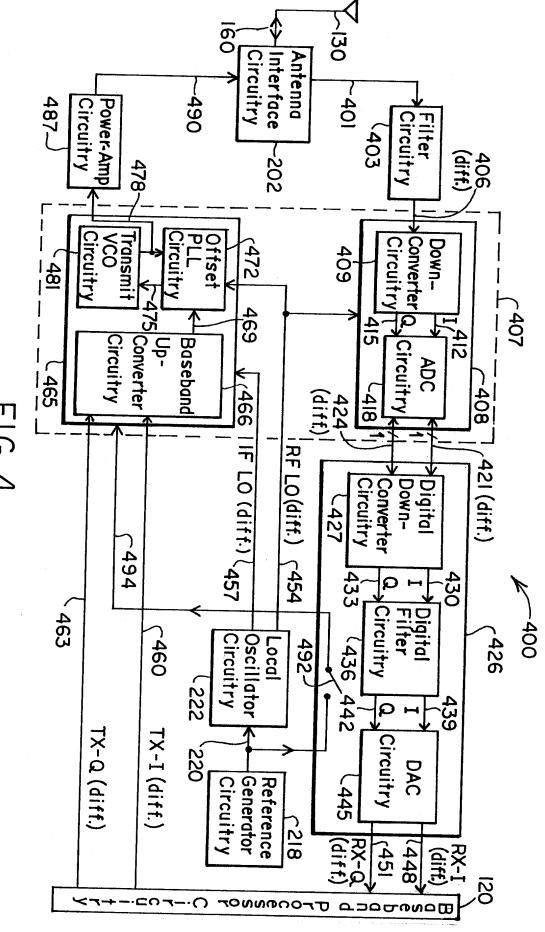
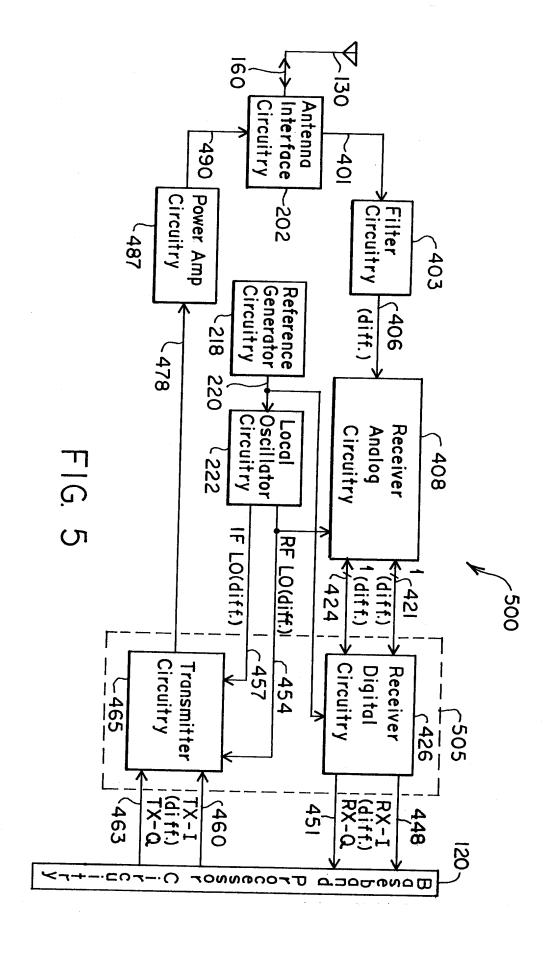
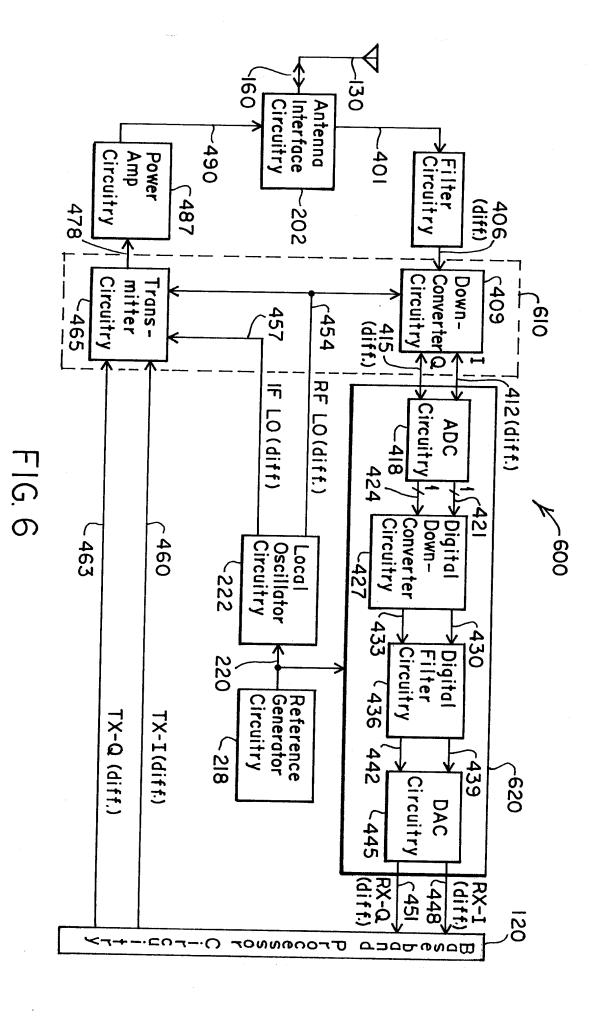
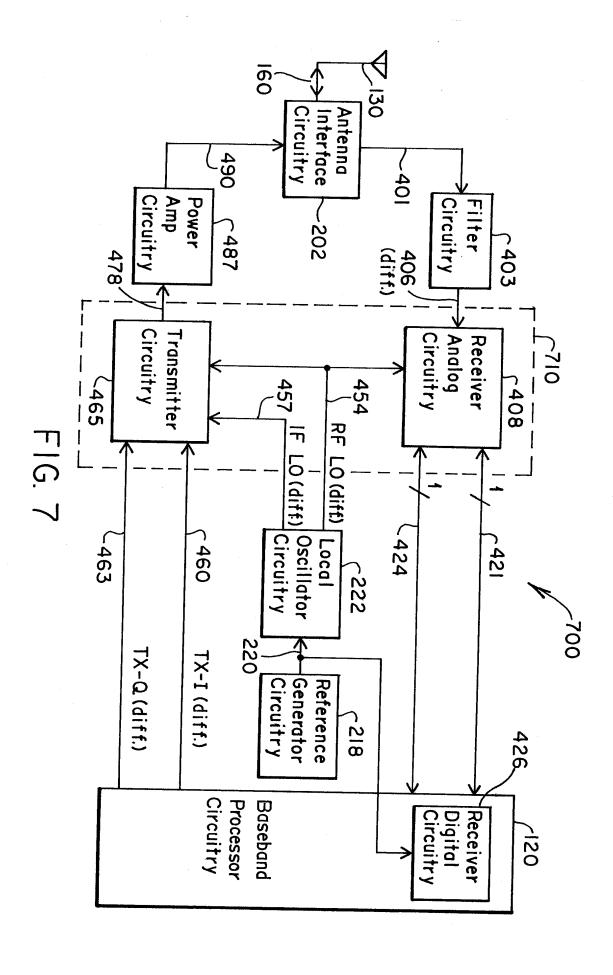
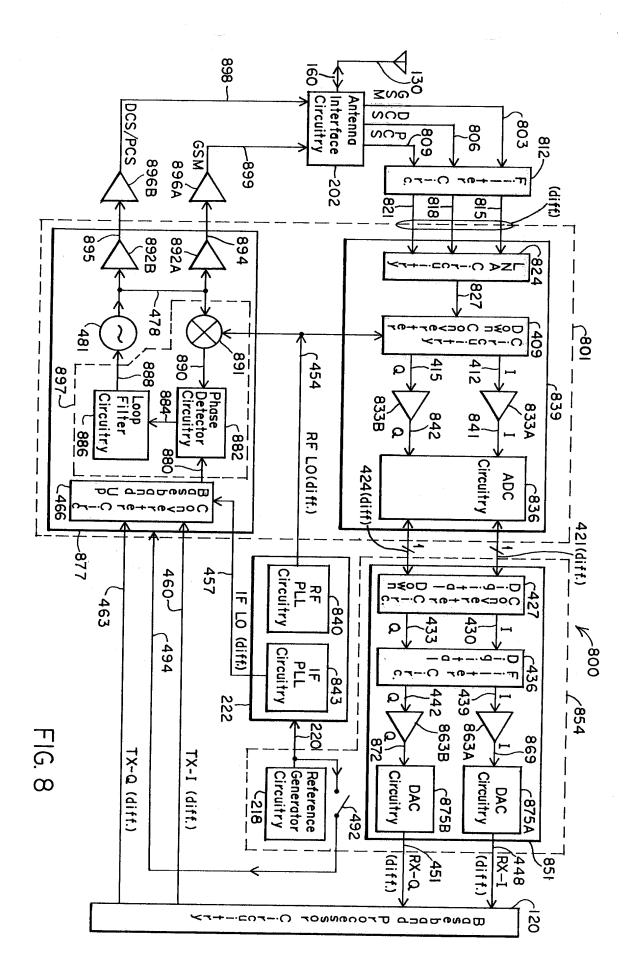


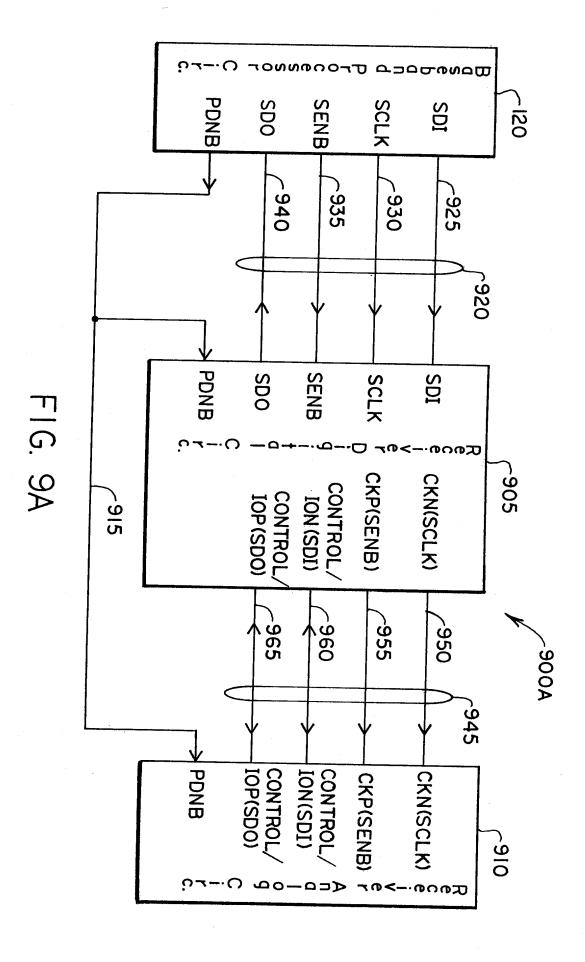
FIG. 4

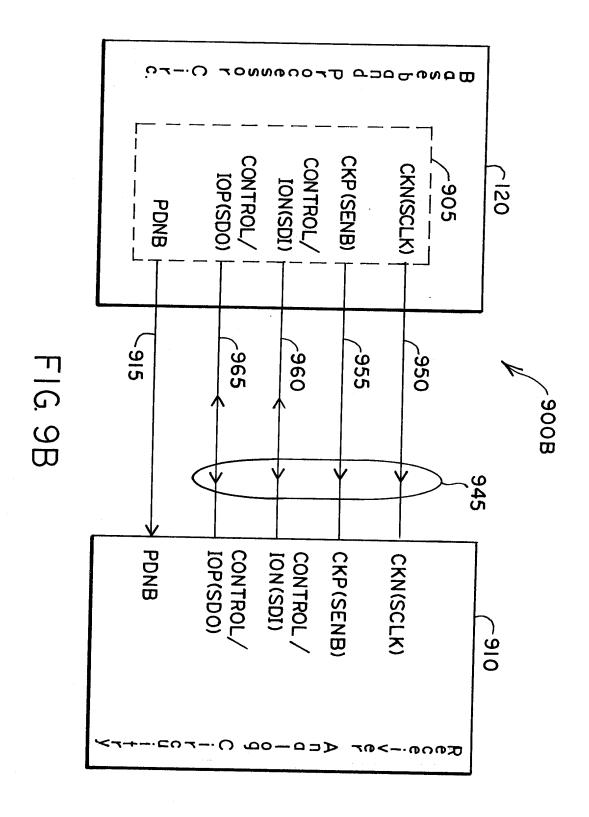


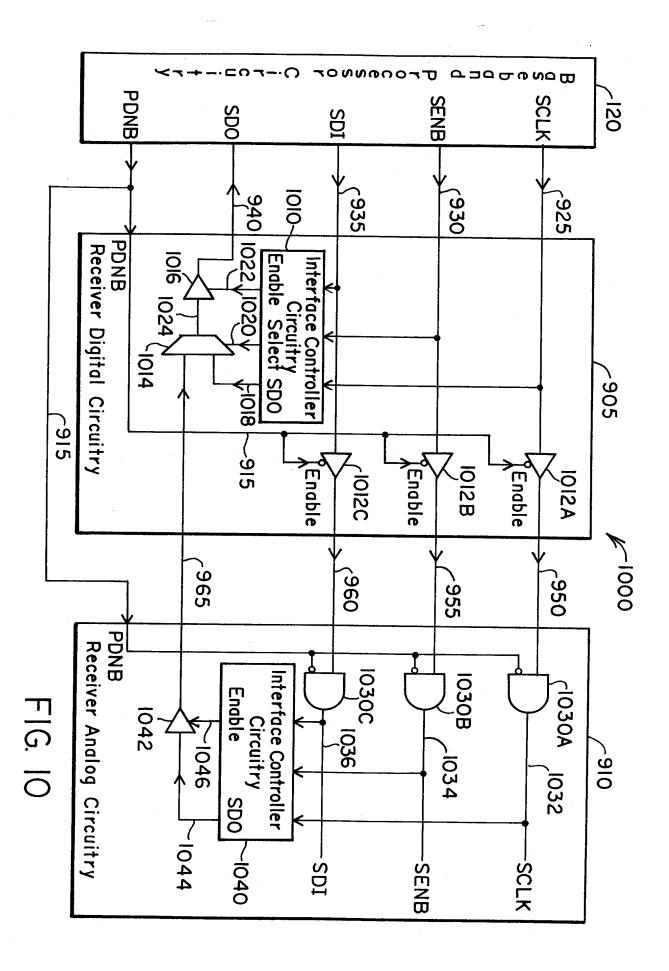












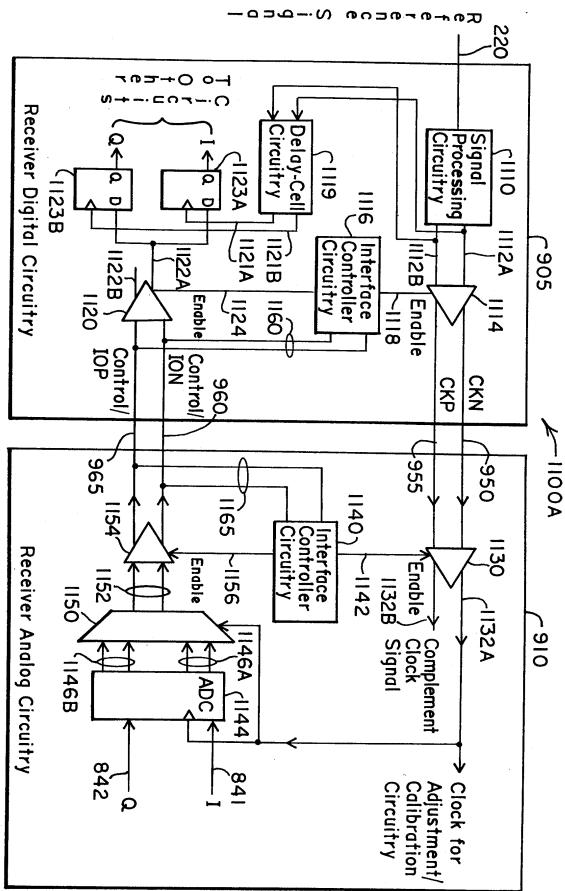
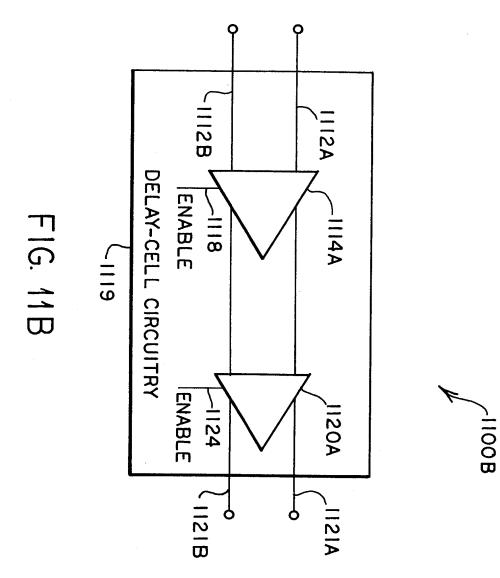
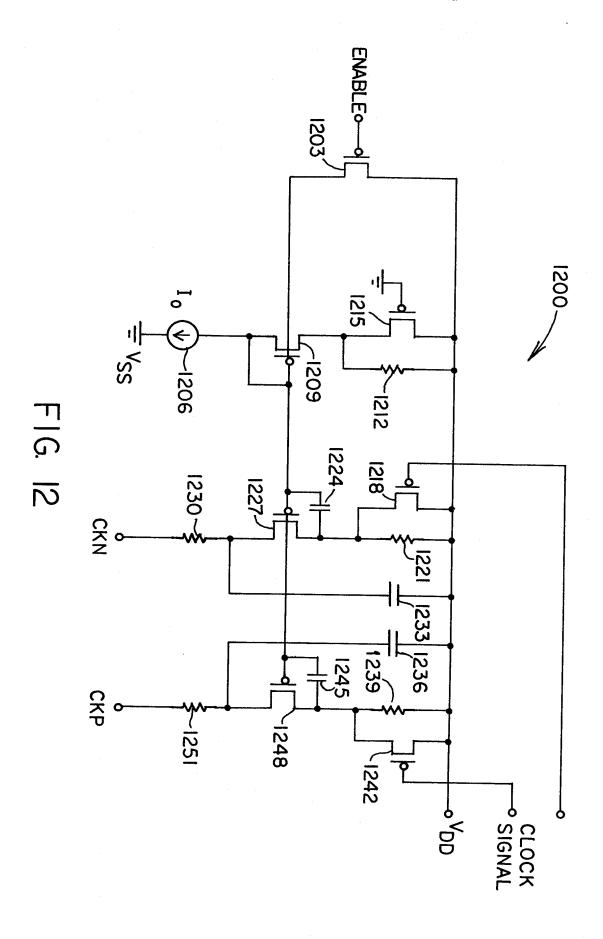
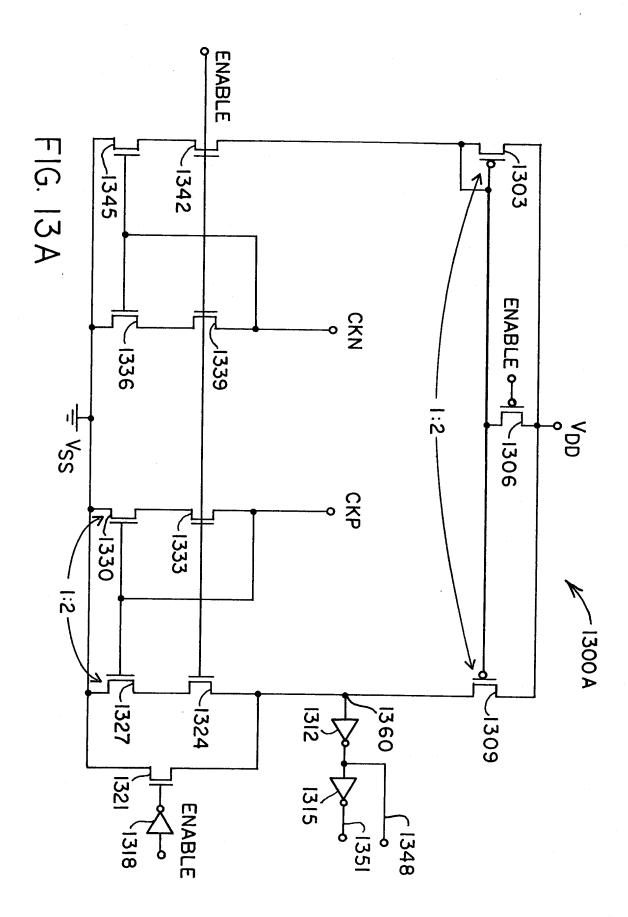
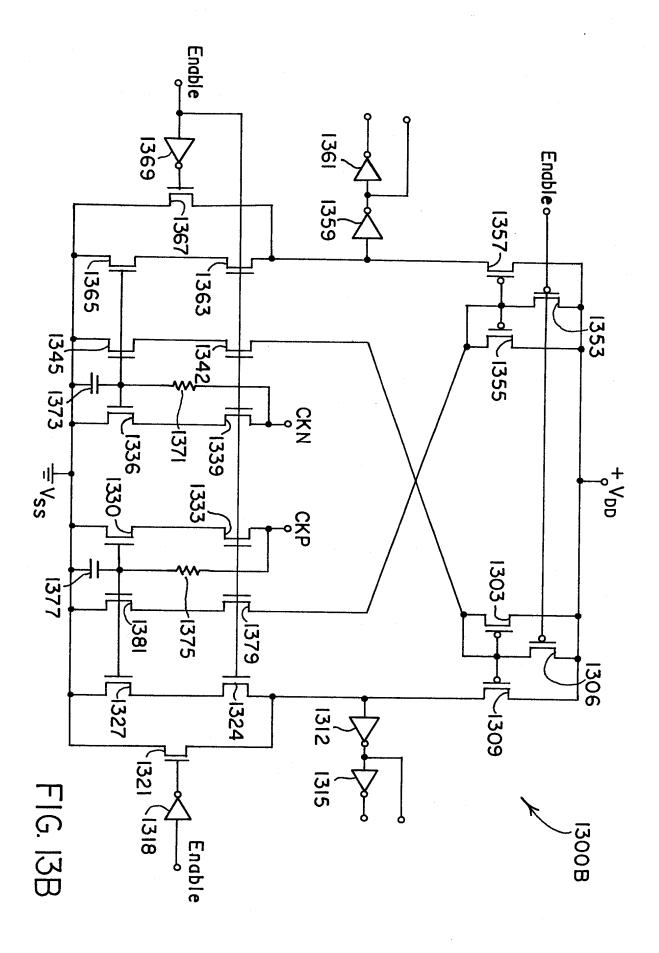


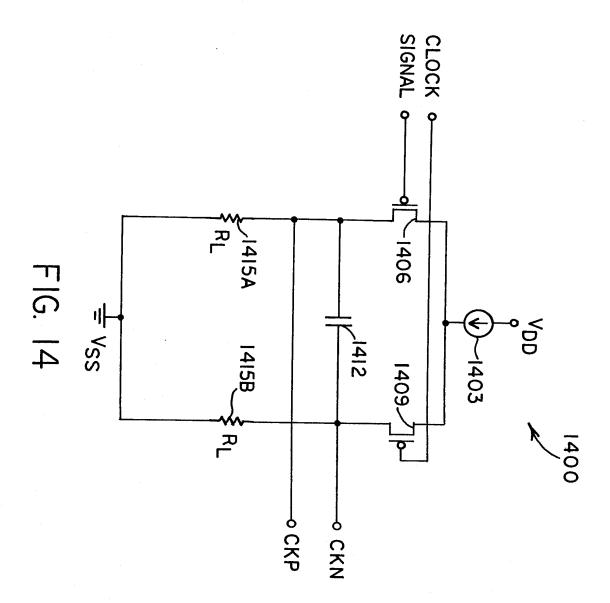
FIG. 11A











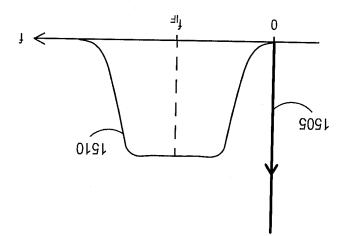


FIG. 15

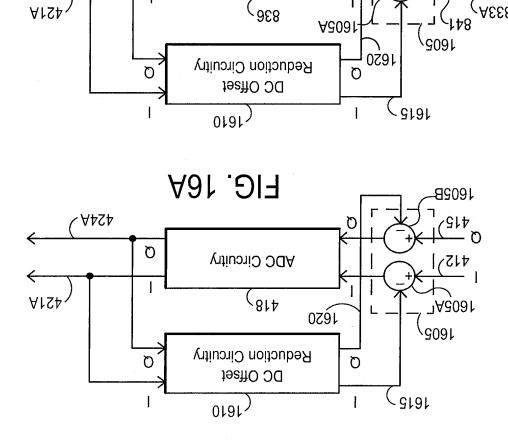
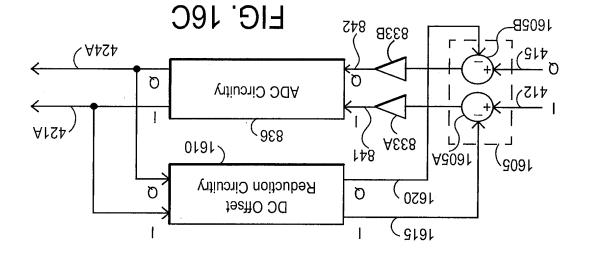


FIG. 16B

ADC Circuitry

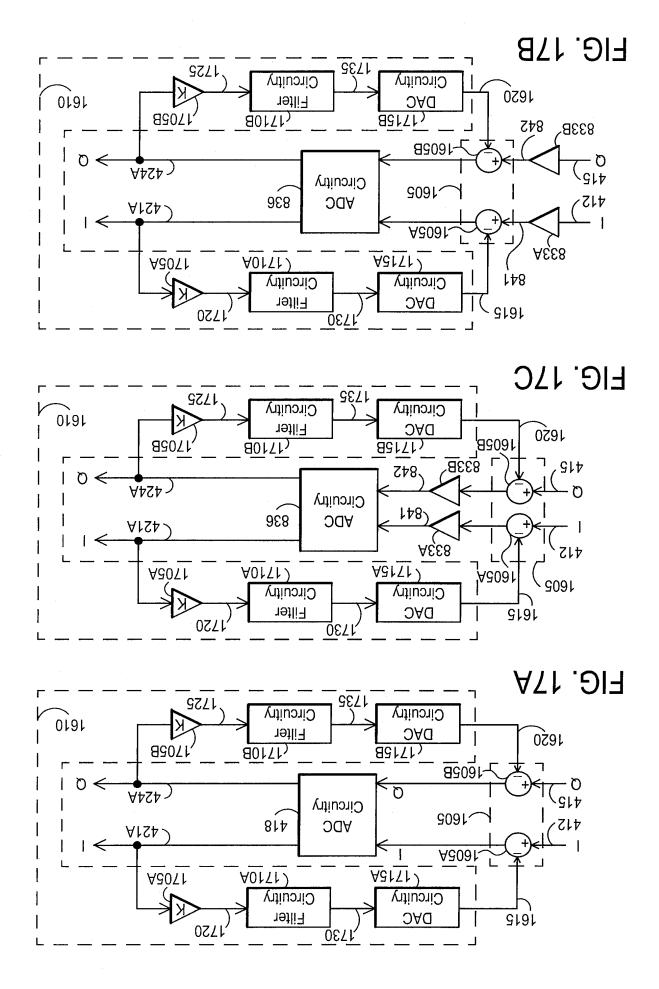
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4605B

833B



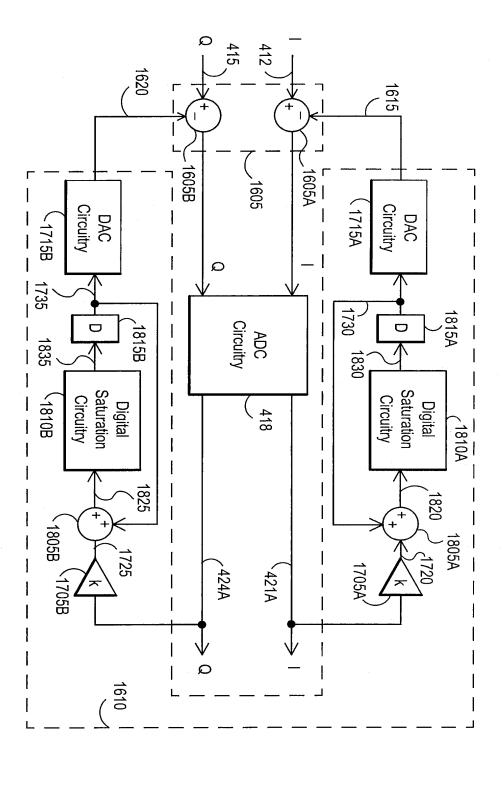


FIG. 18A

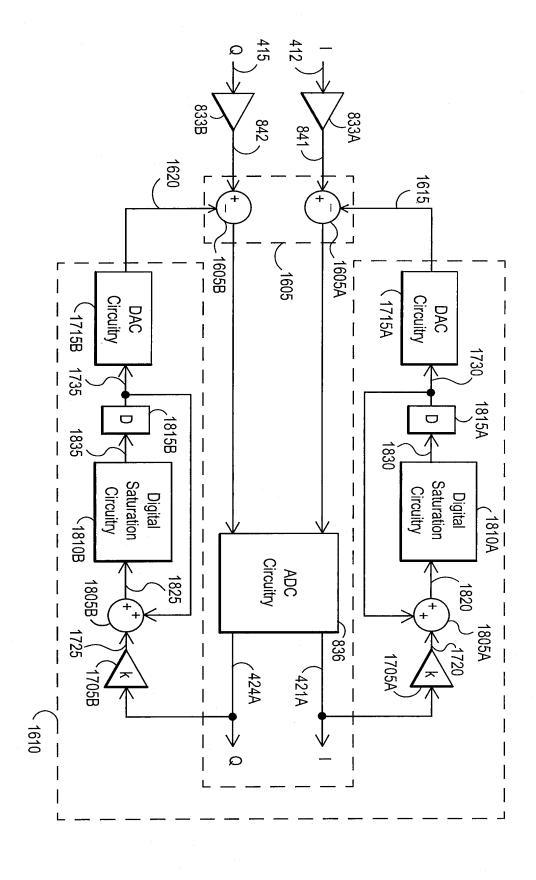


FIG. 18B

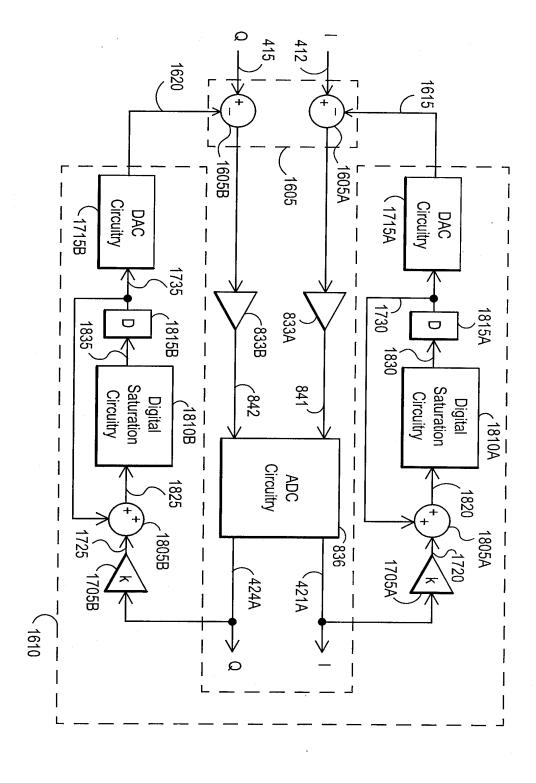
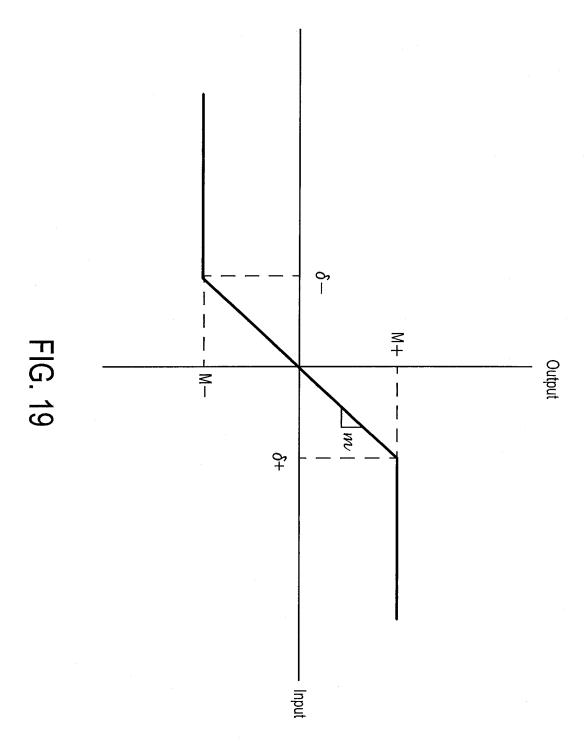


FIG. 18C



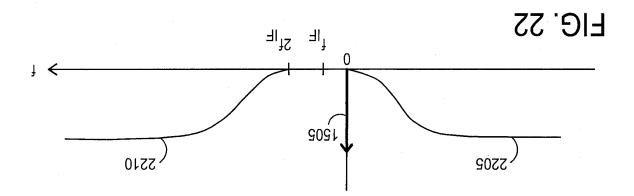
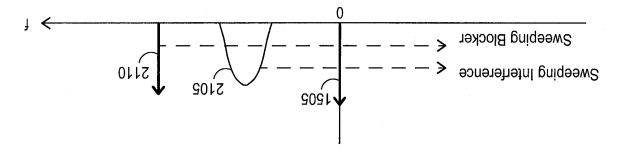


FIG. 21



EIC: 50

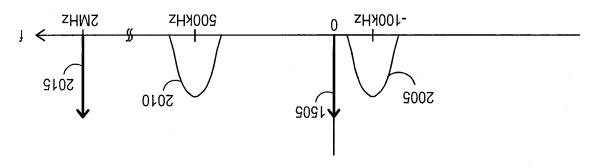
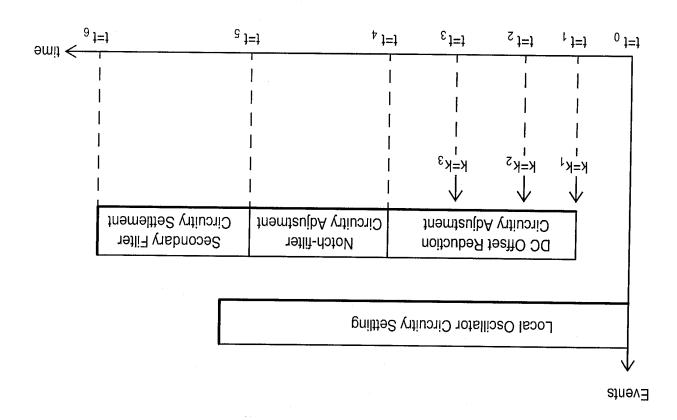


FIG. 24



EIG. 23

2305A

2305A

2305B

2305C

2305B

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